

William Taylor | Compute Games Technology | February 12, 2017

Mandelbrot CUDA Assignment

coursework part 1

[**Github Repository**](https://github.com/wt-student-projects/accelerator-programming)

Table of Contents

[Introduction 2](#_Toc475274733)

[Problem 2](#_Toc475274734)

[Hardware 2](#_Toc475274735)

[Software 3](#_Toc475274736)

[Goals 3](#_Toc475274737)

[CPU Analysis 4](#_Toc475274738)

[Analysis 4](#_Toc475274739)

[Timings 5](#_Toc475274740)

[Profiling 6](#_Toc475274741)

[Sequential Bottleneck 7](#_Toc475274742)

[Double Iteration 7](#_Toc475274743)

[File Chunks 7](#_Toc475274744)

[Action Taken 7](#_Toc475274745)

[GPU Implementation 8](#_Toc475274746)

[Port Overview 8](#_Toc475274747)

[Memory 8](#_Toc475274748)

[Kernel 8](#_Toc475274749)

[Output 8](#_Toc475274750)

[Optimisations 9](#_Toc475274751)

[Kernel Launch 9](#_Toc475274752)

[Early Exit 9](#_Toc475274753)

[Flags 10](#_Toc475274754)

[Timings 10](#_Toc475274755)

[Project Results 11](#_Toc475274756)

[Testing 11](#_Toc475274757)

[Comparison 11](#_Toc475274758)

[Conclusion 11](#_Toc475274760)

# Introduction

## Problem

A snippet of code that renders a section of the Mandelbrot set into an image was given. However, the code is sequential and the code could be improved by using parallel compute to generate the image. In this report I set out the hardware and software I used, the steps I took to port the code to CUDA and conclude how the approach I took resulted in a more efficient program.

## Hardware

I am not using university lab equipment so below are snapshots of the hardware on my laptop. My laptop is equipped with a top of the line GPU and CPU. The CPU is a sixth generation core i7 and is still one of the fastest mobile processors out right now. While it is one generation behind the newly released 7th generation intel processors this CPU is no slouch and will make sure any GPU solution gets a run for its money. My GPU is an NVidia 970m which is not a workstation card like NVidia Quadro K4000 which is the card found in the labs. However, the 970m is a very fast card beating out an NVidia 950 desktop class graphics card. The 970m also comes with additional CUDA cores 1280 compared to K4000 which has 768 allowing more computations to be done in parallel.



## Software

To write the software Visual Studio will be used as its built in profiling tools will help identify expensive sections of the given code with relative ease. The code written will be maintained using Git for version control and is hosted online at Github so issues are tracked well. Obviously as I am writing CUDA code I will be using the latest version of the CUDA toolkit and I will be using the NSight NVidia Profiler to help profile the CUDA kernels written.

* Visual Studio
* CUDA Toolkit 8.0
* GitBash + GitHub
* NSight NVidia Profiler

## Goals

For the project I set some goals on which I could judge its success. The first goal was to output bit for bit the same image. Accuracy was of incredible important as a faster version is only applicable in real world use if it outputs the same image. Secondly a significant speed improvement is desired, this should be a certainty as the sequential CPU code will not be touched. Finally, the interface for running the program should be the same. Currently the program accepts two arguments which represent width and height for the outputted image, the CUDA port of this program will work in the same way for consistency. Following these goals will result in an executable that behaviors exactly as the original, produces an identical result but does so in a fraction of the time thus proving the advantage of CUDA in speeding up intensive applications.

Identical Image Output- Large Speed Improvement- Identical Interface

# CPU Analysis

## Analysis

My first task was in doing a quick analysis of the given code to look for immediate performance improvements. One of the first things identified was a double iteration.



An unneeded row vector was also identified which wasn’t really needed as all it did was point to section of the image. While this would have little effect on the original code, once I ported it to CUDA it would mean less memory to transfer on the device and fewer calls to the CUDA API.



Additionally, there was a stack allocated array which at least in debug mode where no optimizations where used made the code slower as this array was being allocated every time a color was set for the image. However as expected it was optimized out when optimization flags were turned on.



When writing the file to disk the image is reversed and written in chunks. While the image will still need to be saved like this in the CUDA version to match the output for the given code, a much better improvement would be to have the code that generates the image to do it in reverse instead and then it can be directly written to disk rather than writing it in chunks.



Finally, there was some missing if blocks which could have stop unneeded code from executing. If the first if statement validates to true it does not need evaluate the second if statement or enter the do while block. While a tiny improvement it is, it is a small improvement that could go a long way.



## Timings

For timings I took each function and benchmarked it with various image sizes and took the average from 10 runs of the function. All optimizations were turned on and was compiled as a 64bit executable. Below you can find the breakdown of the results. As we can see the most expensive parts of the application are the ***calc\_mandel*** function and the ***screen\_dump*** function as expected. With this assumption proven correct and backed up by sufficient evidence I then when to profile the code to look at the most expensive sections of the code to have an idea of the gains to be made by the enhancements I had identified earlier and with the added benefit of utilizing CUDA to parallelize the code where needed.

## Profiling

To profile the sequential code, I was relying on the profiling tools inside Visual Studio. Not only do they present a nice report breaking down the function timings but it also highlights the code in the editor which is handy. Running the given code through the profiler produced predictable results.

### Sequential Bottleneck

The biggest bottleneck identified is what I am calling the ‘Sequential Bottleneck’. The issue is that code highlighted on the right is performed sequentially for each pixel in an image. However, as it doesn’t reply on input data or adjacent elements in the image it is perfectly possible to do this operation in parallel rather than sequentially. It also explains why the program doesn’t scale well as the number of operations scale exponentially with larger image sizes. For a 256x256 image this operation is executed 196608 times but for a 4096x4096 image this is 16777216 times. With the code ported to CUDA we will be able to parallelize this operation and no longer be bound by this limitation.

### Double Iteration

Another big bottleneck that came up in the profiler was the double iteration behavior of the code that will perform two loops across a given image. The first loop calculates the index for a color from a map the next loop then goes through the image again and assigns it a color from the map based on the value currently contained within that pixel which is the index from the first loop. Obviously these two loops can be merged and by doing so we will see a large gain in performance.

### File Chunks

The final bottleneck found in the application was due to how the file was being written to disk. In the given code the image is reverse with each row being written at a time rather than the entire file being flushed in one call. It would be much faster just to write the entire image to disk rather than just write each row individual till the full image has been written.

This however brought about a problem. While the image looked the same if you were to write the entire image at once instead of reversing it, it was not. Doing a simple binary check on the output reveals that the output image is not mirrored horizontally. Thus to ensure the output remains the same while writing the image in one go, the given code will need to be adapted so it calculates the flipped image and write that to disk rather than the normal image then write it to disk flipped on its Y axis. By doing so we will remove this small bottleneck and instead of writing rows individually to disk do it all in one go which will certainly be faster.

### Action Taken

With these bottlenecks identified they would be removed in the CUDA port through the following steps. First the sequential bottleneck would be solved by parallelizing the calculation of each pixel’s value. The double iteration would be solved by merging the two loops into one. Finally, when writing the output, it would be done in one go by calculating the image in reverse so it doesn’t need to be written in reverse.

# GPU Implementation

## Port Overview

### Memory

The first part of the port was taking data that would need to be accessed on the device and making it available. This was done just by making the data constant memory. This would yield benefits as constant memory is always cached allowing for fast reads on the device.



Additionally, global memory is allocated which is used to store the image that will then be written to disk. I use my own little helper class to help manage this for me. But what it does is very simple. It allocates global memory the sets a default value for it.



### Kernel

The key section of the port was to parallelize the operation that calculates the output color so instead of sequentially calculating each pixel they can be done in parallel resulting large performance gains. The kernel is very simple it generates an index for the section of the Mandelbrot set it will work on.



### Output

Once the output has been calculated and all value written to the global memory allocated we just move the device memory back into host memory so it can be written to disk. Once it has I just flush the memory to disk without reversing the data as the kernel generates the image in reverse order so there is no need to write it in reverse order.



With all these components in place I had I CUDA program that would produce the image that was required. However, this implementation was very basic and further optimizations were made to ensure the code was as performant as possible before comparing it to the given CPU only code.

## Optimisations

### Kernel Launch

In my first implementation kernels were one dimension meaning larger image sizes could not be generated as it breached the max block or grid size. To overcome this issue launching the kernel was rewritten to do a two dimensional launch allowing the program to generate larger image sizes. Additionally, I used to CUDA Occupancy API to get an estimate for the grid and block size parameters for the given kernel. One shortfall of this API is that it is designed for one dimension workloads so I had to write some additional code that converted this estimation so it could be used in a two-dimension kernel launch.



### Early Exit

In the first implementation in all cases the kernel would never exit early and continue to the end of the function. To stop this happening the if statements were reworked to return immediately if they were met.



### Flags

I also looked at any additional flags that could be set for an increase in performance. –use\_fast\_math was used however to keep the output the same --fmad=false. This was because this optimization resulted in less accurate results as this optimization estimates the operation rather than performing it completely leading to slight differences. As I wanted an identical output this had to be turned off however the other flags enabled by -use\_fast\_math was left on. Additionally, I upgraded the compute version and architecture to ensure there were no limits on thread and block size on my modern graphics card.

## Timings

The following optimisations resulted in a significant speed up as can be seen in the below table.

# Project Results

## Testing

Throughout this project outputs were tested thoroughly to ensure the output is the same as the given code. As it was one of my own key goals. The first test was some C++ unit tests I wrote using visual studio’s unit testing framework to make sure the data in the files matched as well, there are also additional tests for checking height so if the same data test fails I can double check to make sure I got both programs to output the same image size. A nice feature of this testing framework is it integrates into Visual Studio very well. However, to ensure that a false test doesn’t make its way through my tests I also used the file compare utility in binary mode found on windows to double check my results.

## Comparison

## 

## Conclusion

As expected the code ported to CUDA resulted in significant performance increase thanks to the added performance found in parallelizing the code. With a large output image, we saw a 40x performance increase and even with a smaller image we saw a 2x performance increase. Not only is the code faster but the CUDA code will scale better with even larger images making it superior to the original in every way possible.